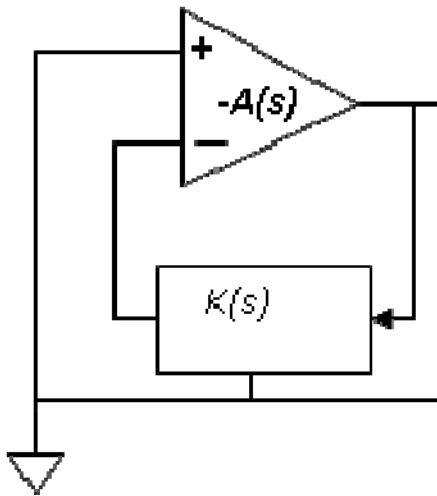


Quick Introduction to Phase Margin

Excess phase shift in an operational amplifier can cause oscillation in the user's application

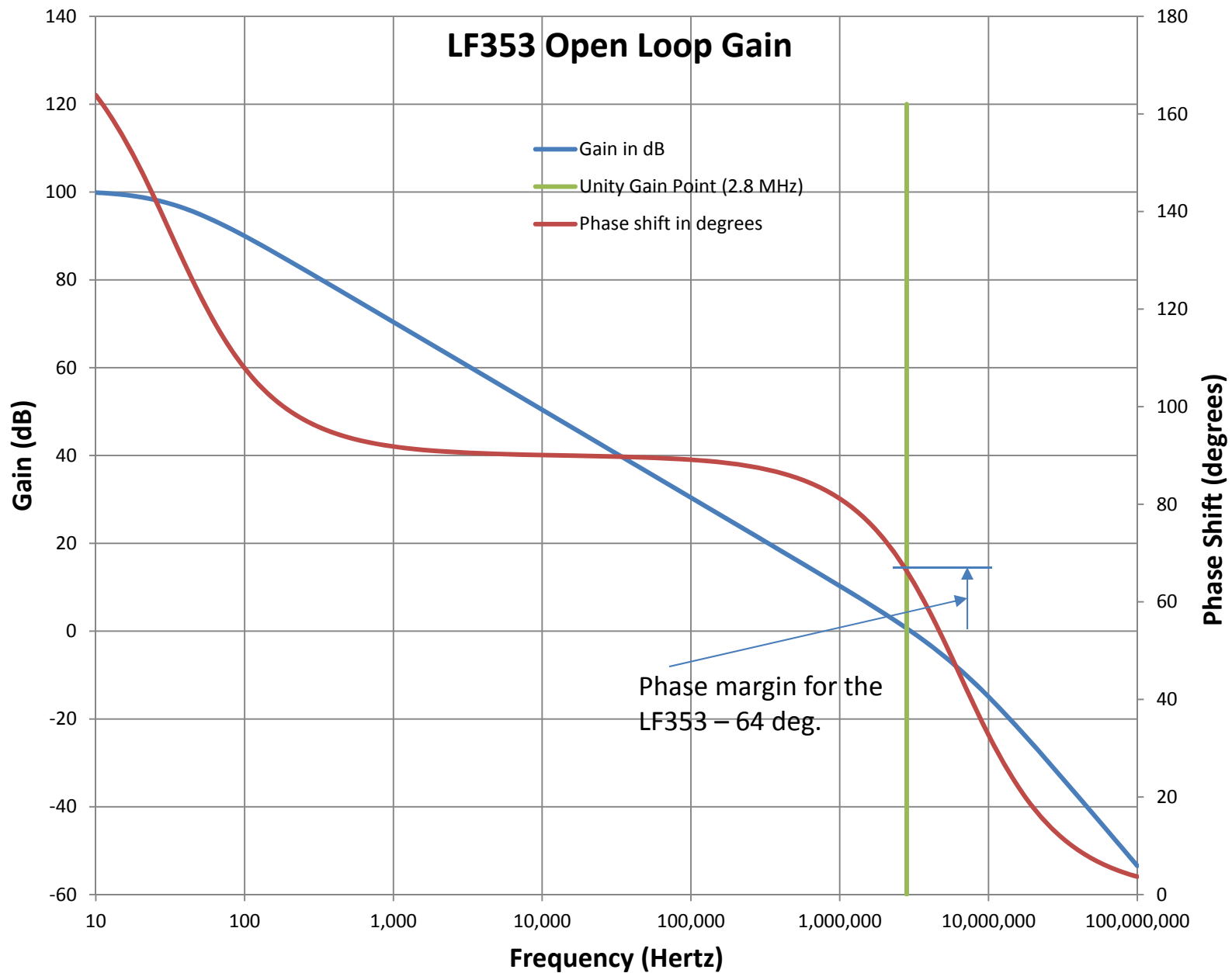
Defining Phase Margin

- Applies to feedback around an amplifier with negative gain at low frequency
- If $|Gain| > 1$ and phase shift > 180 degrees, the circuit oscillates
- If phase is too close to 180 degrees gives overshoot to step input
- Phase margin for the amplifier is how many degrees of phase shift could be added too the loop gain by the feedback block $K(s)$ before oscillation would start

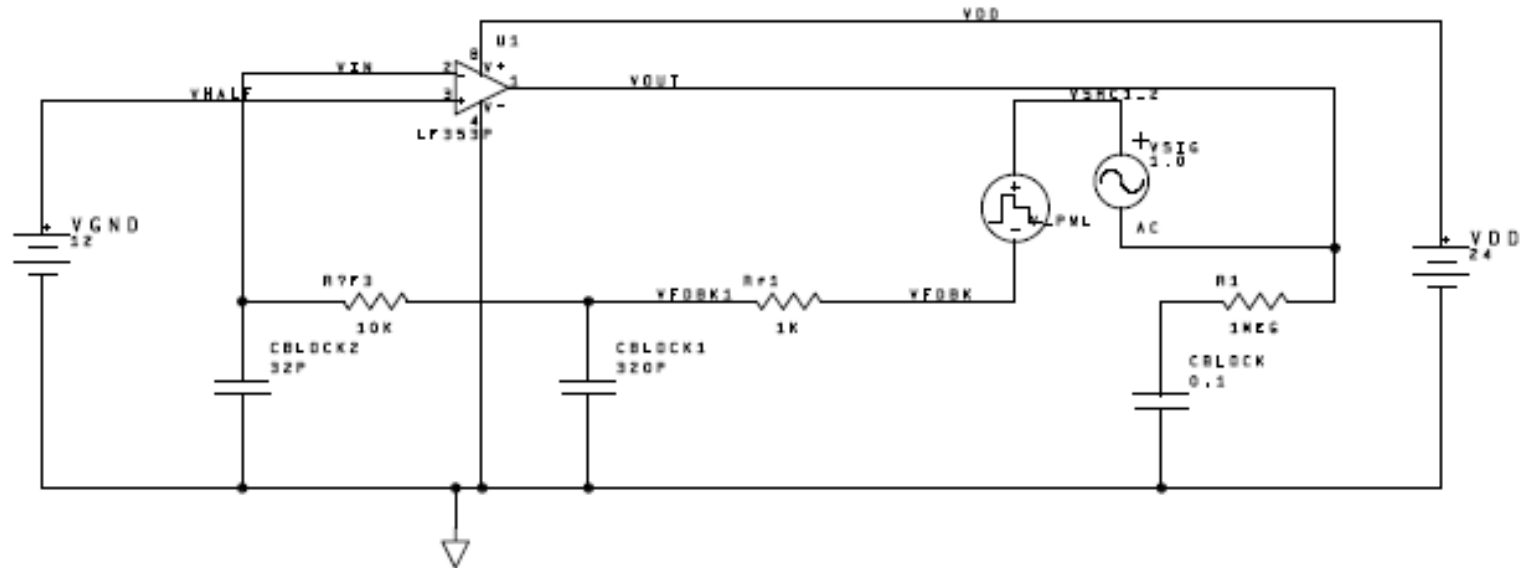


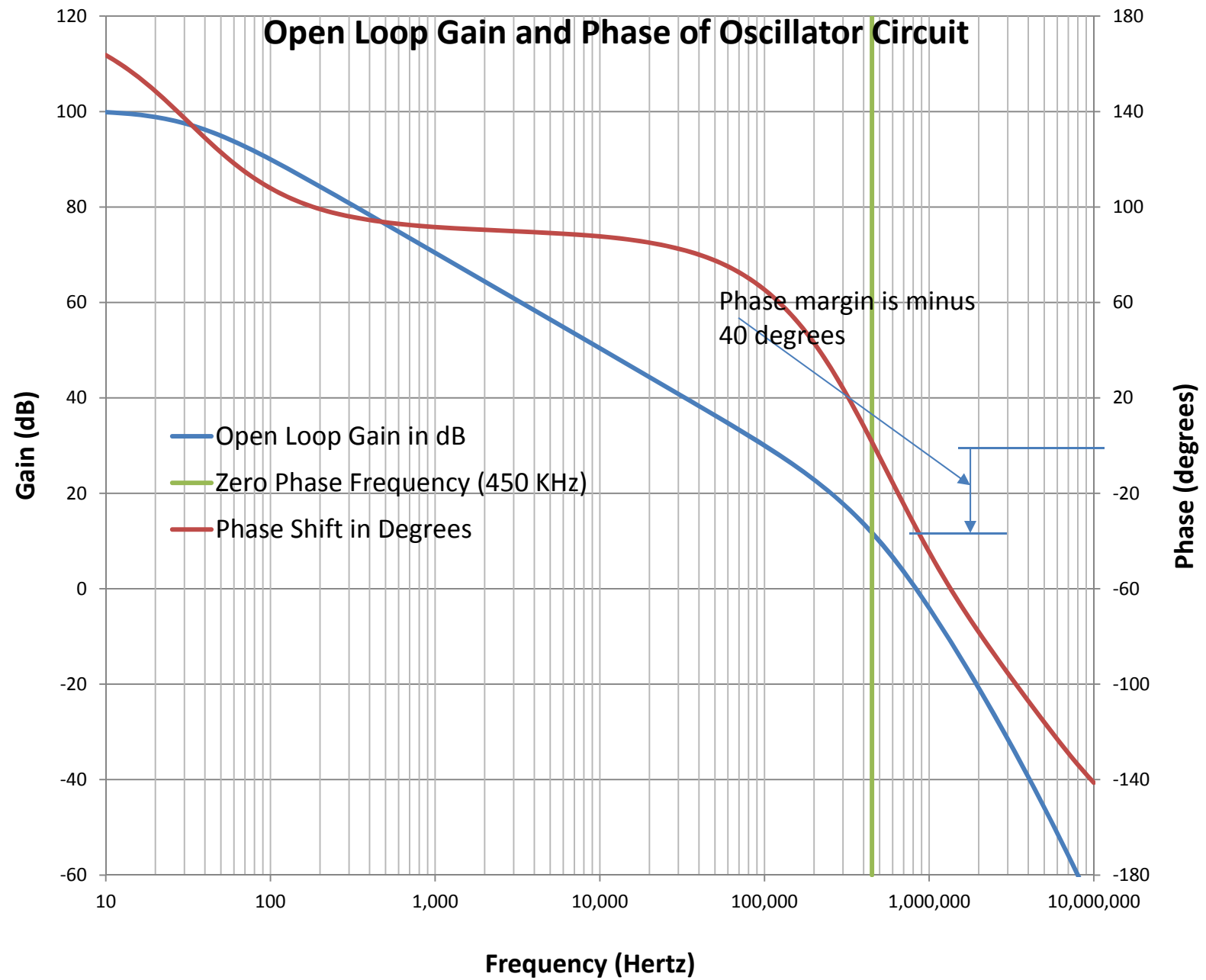
Open loop gain is defined as: $G_{OL} \equiv -A(s)K(s)$

Usually phase is a monotonic function so the phase margin is $180 - \text{Phase}(A(s))$ at frequency for which $|A(s)| = 1X$ (0 dB)

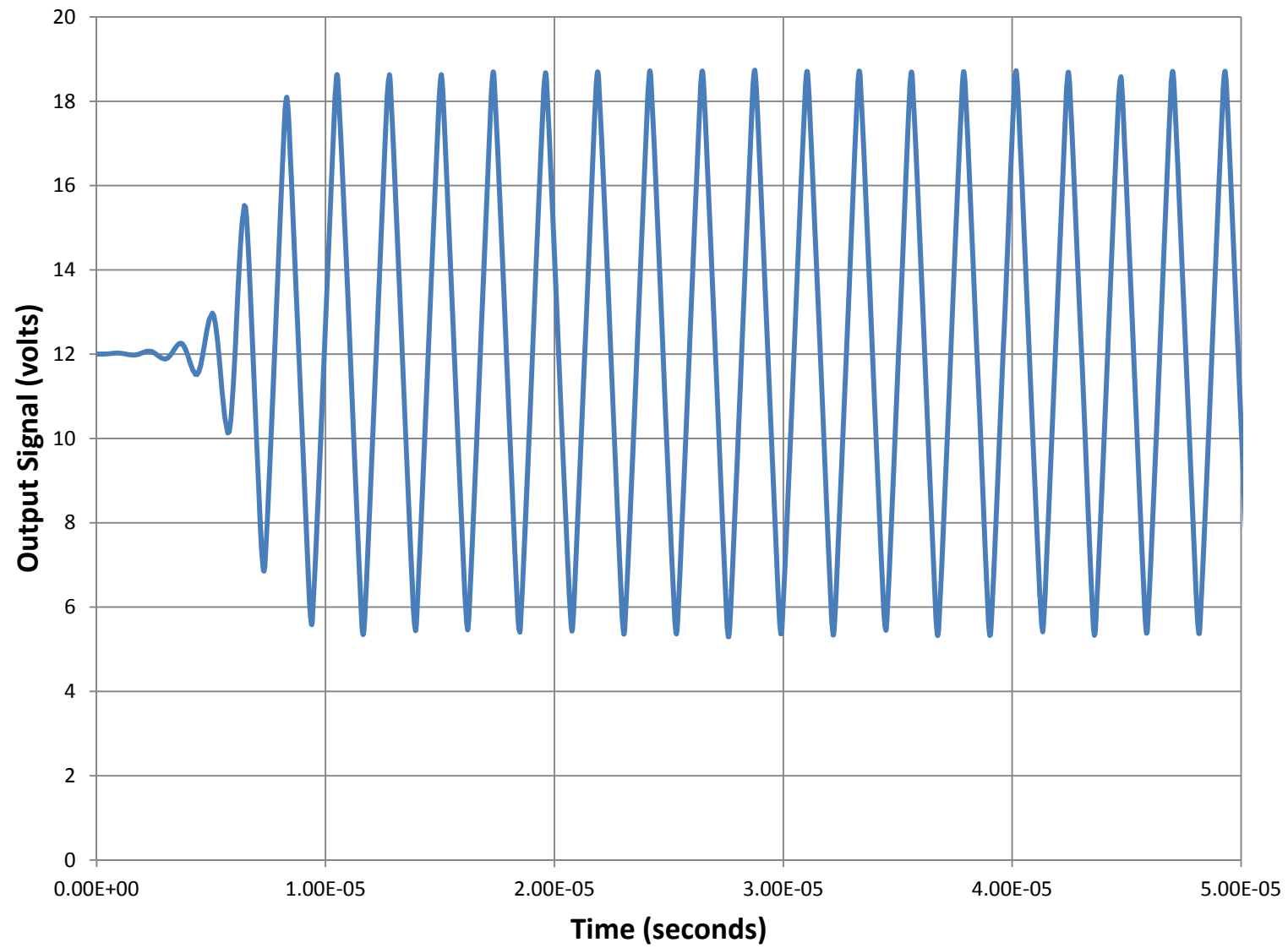


Schematic of Phase Shift Oscillator



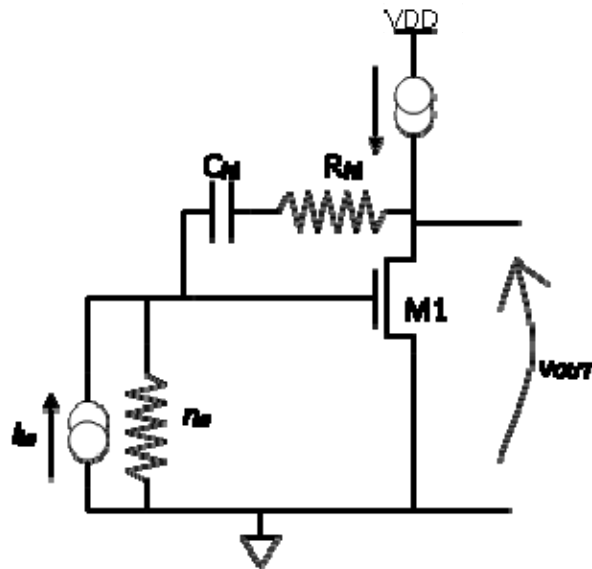


V(vout)

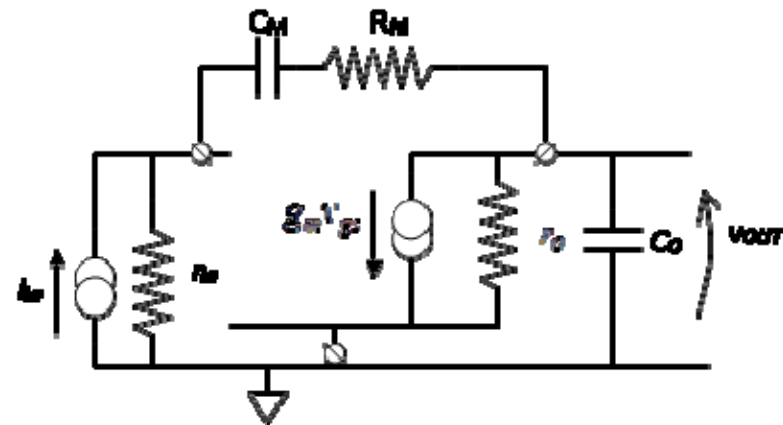


Common Source Stage with Miller Compensation

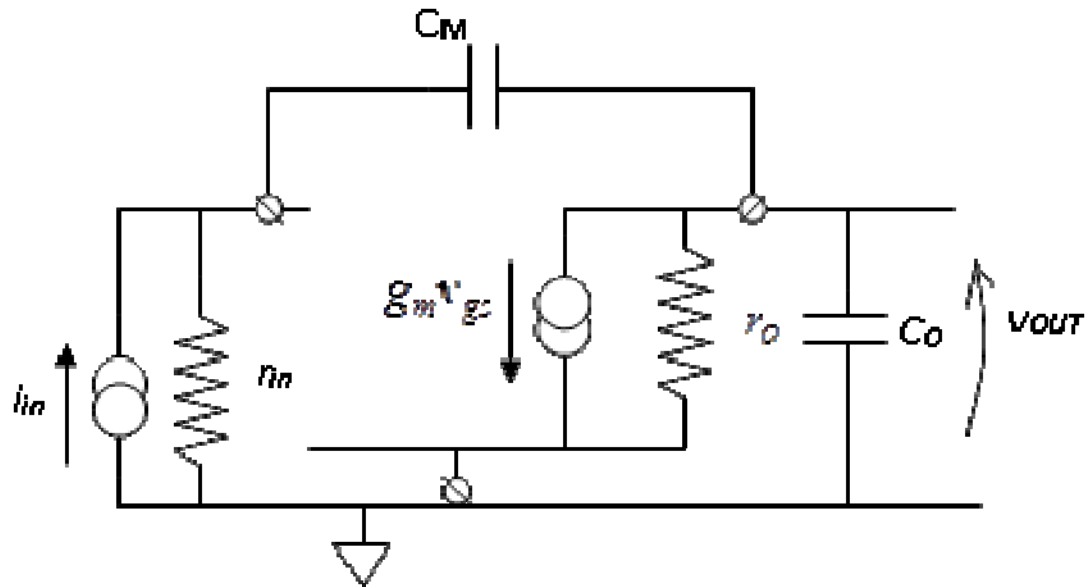
- Complete Circuit



- Small Signal Model
 - Omits CGS and CGD



Transfer Function with $R_M = 0$



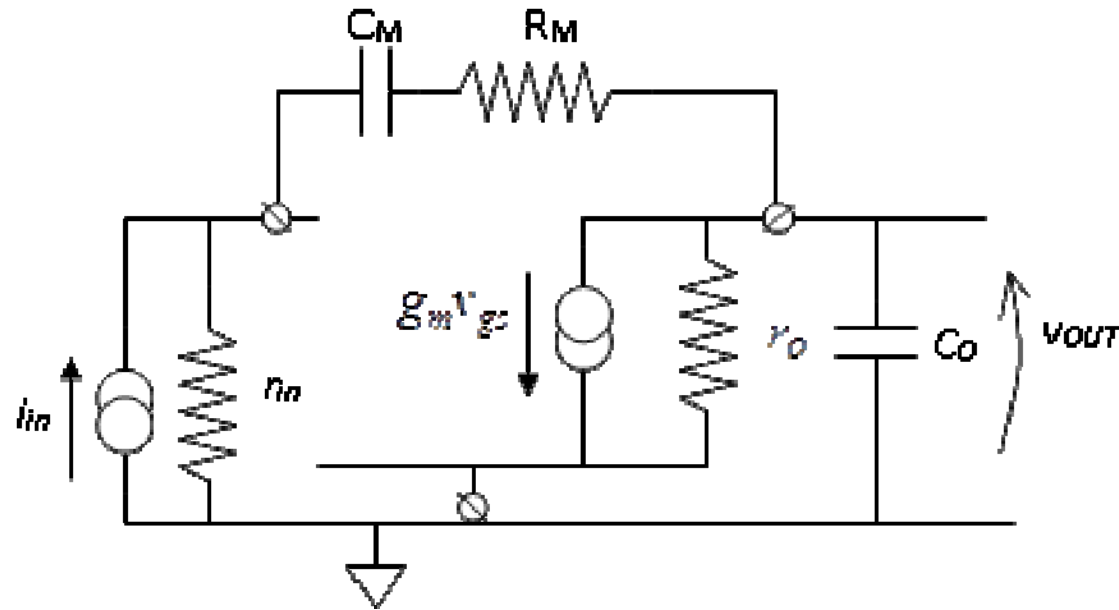
$$H(s) = \frac{-g_m r_o (1 - s C_M / g_m)}{s^2 C_o r_o C_M r_i + s((1 + g_m r_o) C_M r_i + C_o r_o + C_M r_o) + 1}$$

Zero – Bad for phase!!!

Second pole

Miller effect
Dominant pole

Transfer Function with RM in Place



Zero term bad for phase

Zero – New term good for phase
Make $R_M > 1/g_m$

$$H(s) = \frac{-g_m r_o (1 - s C_M / g_m + s C_M R_M)}{s^2 C_o r_o C_M (r_i + R_M) + s((1 + g_m r_o) C_M r_i + C_o r_o + C_M r_o + C_M R_M) + 1}$$