

# Bounded-width polynomial-size Boolean formulas compute exactly those functions in $AC^0$

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Communicated by D. Dolev; received 2 April 1992; revised 16 December 1993

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## Abstract

We show that the complexity classes  $AC^0$  and  $NC^1$  consist exactly of, respectively, constant and  $O(\log n)$  width polynomial-size Boolean formulas.

*Key words:* Computational complexity; Boolean functions; Circuit complexity

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## 1. Introduction

The complexity classes  $AC^0$  and  $NC^1$  and their modifications are well studied in the literature. For example, Furst et al. [4] and independently Ajtai [1] proved that  $AC^0 \neq NC^1$ . Spira [5] showed that a Boolean function is computable by polynomial-size formulas iff it is computable by logarithmic-depth circuits. In other words, the class  $NC^1$  consists exactly of polynomial-size formulas. Barrington [2] gave another interpretation by showing that  $NC^1$  consists exactly of those functions computed by bounded-width polynomial-size branching programs. In this paper we consider similar characterizations of  $AC^0$  and  $NC^1$  via the width of Boolean formulas.

## 2. Definitions

In this section we review some of the basic notions from Boolean circuit complexity (for more details see, for example, [6] and [3]).

A *Boolean circuit* on  $n$  Boolean variables  $x_1, \dots, x_n$  is a directed acyclic graph with the following properties. Each node of fan-in zero, called *input*, is labeled with a variable, the negation of a

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variable, or the constants 0 or 1. The nodes of fan-in greater than zero, called *gates*, are labeled with AND or OR function. Lastly, there is a single sink node of fan-out zero called *output*. The *size* of a circuit is the number of its gates, and the *depth* is the length of the longest path from an input to the output.

A Boolean circuit computes a Boolean function  $f : \{0, 1\}^n \rightarrow \{0, 1\}$  in the natural way, i.e., for every  $b = (b_1, \dots, b_n) \in \{0, 1\}^n$  we let  $f(b)$  be the result of the output gate when the tuple  $(b_1, \dots, b_n)$  is given to the corresponding inputs. The class  $AC^0$  consists of functions computed by polynomial-size circuits with unbounded fan-in and constant depth, and  $NC^1$  is the class of those functions computed by circuits of fan-in two and depth  $O(\log n)$  (note that here polynomial-size comes for free).

A *Boolean formula* is a circuit whose gates have fan-out at most one. Since the size of a Boolean formula does not count the inputs, we can attach to each gate that has an input its own copy of the input. In this way we can conveniently represent the Boolean formulas as trees and, in fact, we use the two terms interchangeably. A formula that can be represented by a binary tree is called a *binary formula*.

The *width* of a Boolean formula is formally defined using its underlying tree. Given a tree  $T$ , we first level the tree; that is, level 0 contains the root, level 1 contains the children of the root, level 2 contains the children of the children of the root, and so on. Let  $w_l$  be the number of nodes on level  $l$ . The width  $w$  of the tree  $T$  is simply  $w = \max\{w_l \mid 0 \leq l \leq d\}$ , where  $d$  is the depth of  $T$ . Of course, the width of a Boolean formula is the width of its underlying tree.

We define  $BFW^0$  and  $BFW^1$  to be the classes of Boolean functions computed by polynomial-size Boolean formulas that have, respectively, constant and  $O(\log n)$  width.

Finally, we use  $s(F)$  and  $w(F)$  to denote the size and width of a formula  $F$ .

### 3. Preliminaries

In this section we first discuss an easy tree property about the width. After that we present several technical simulations needed for the proof of our main result.

There is a natural way to assign the notion of width to particular nodes of a tree  $T$ . Namely, the width of a node is the width of the subtree rooted at the node. Clearly, the width of the tree  $T$  is the width of its root. Also, all the nodes that have full width of  $T$  lie on a path from the root. This follows from the fact that no two children of a node can have the same width as the node itself. To see this, suppose a node  $x$  has width  $w_x$  and children nodes  $y$  and  $z$  of width  $w_y$  and  $w_z$ , respectively. Denote by  $T_y$  and  $T_z$  the subtrees rooted at the nodes  $y$  and  $z$ , and let  $l_y$  and  $l_z$  be the levels at which  $T_y$  and  $T_z$  attain the width. We now argue that  $w_x = w_y = w_z$  is not possible. Indeed, if it were, then  $l_y \neq l_z$  since otherwise one would have  $w_x \geq w_y + w_z = 2w_x$ , a contradiction. Therefore, either  $l_y < l_z$  or  $l_y > l_z$ . But if  $l_y < l_z$  then  $w_x \geq w_y + 1 > w_x$ , a contradiction. Similarly, if  $l_y > l_z$  then  $w_x \geq w_z + 1 > w_x$ , again a contradiction. This completes the proof of the fact. The path along which lie all the nodes that have full width of  $T$  is called the *trunk* of the tree  $T$ .

**Lemma 1.** *A formula of width  $w$  and size  $s$  on  $n$  variables can be computed by a binary formula of width  $2w$  and size  $2^w ns$ .*

**Proof.** We argue by induction on the width  $w$ . The case  $w = 1$  is trivial, and for  $w > 1$  consider the trunk of the tree  $T$  representing a formula  $F$  of width  $w$  and size  $s$ . To simplify the argument, we suppose that the trunk contains exactly two nodes; it will be clear later what modifications are

needed in case the trunk contains one or more than two nodes. Denote the two gates  $G_1$  and  $G_2$  and suppose they are, say,  $\wedge$  and  $\vee$  node, respectively. Further assume that  $G_1$  is at the root, and  $P_1, \dots, P_k$  are its subtrees of depth greater than 0 not including the subtree rooted at the node  $G_2$ . Also, denote  $p_1, \dots, p_u$  the depth-0 children of  $G_1$ , i.e., all the literals feeding into it. Next, let  $Q_1, \dots, Q_m$  be all the subtrees of depth greater than 0 of the node  $G_2$ , and  $q_1, \dots, q_v$  its depth-0 children. We can assume that both  $u$  and  $v$  are at most  $n$  and no constants feed into  $G_1$  or  $G_2$ , since otherwise the formula  $F$  is determined or we can remove the gate  $G_2$ .

Since the width of each  $P_i$  ( $i = 1, \dots, k$ ) and  $Q_j$  ( $j = 1, \dots, m$ ) is less than  $w$ , by the induction hypothesis we have equivalent binary formulas  $P'_i$  ( $i = 1, \dots, k$ ) and  $Q'_j$ , ( $j = 1, \dots, m$ ). Moreover, for every  $i = 1, \dots, k$  and  $j = 1, \dots, m$ ,  $w(P'_i)$  and  $w(Q'_j)$  are at most  $2(w-1)$ , and  $s(P'_i) \leq 2^{w-1}ns(P_i)$  and  $s(Q'_j) \leq 2^{w-1}ns(Q_j)$ .

We now build the binary tree  $T'$  that simulates  $T$  as follows. The top gate of  $T'$  is same as  $G_1$ , i.e., an  $\wedge$  gate. The right subtree of the root is the binary tree  $P'_1$ , and the left subtree begins with a line of  $\wedge$  gates whose other input is the constant 1. The number of  $\wedge$  gates on the line is equal to the depth of  $P'_1$ . Next, the last gate on the line has the right subtree being the binary tree  $P'_2$ . Again, the left subtree of the last gate begins with a line of  $\wedge$  gates whose other input is the constant 1, and the number of them is equal to the depth of  $P'_2$ . Continuing in this way, we partially build the tree  $T'$  that has as the bone a long line of  $\wedge$  gates except the last gate, and the other input of the  $\wedge$  gates is either the constant 1 or one of the binary trees  $P'_1, \dots, P'_k$ . Finally, we extend the bone down with a line of  $u$   $\wedge$  gates whose other inputs are the literals  $p_1, \dots, p_u$ . Now, the last  $\wedge$  gate on the line has one input  $p_u$ , and the other input is the same node as the gate  $G_2$ , i.e., an  $\vee$  gate. The right subtree of the  $\vee$  gate will be the tree  $Q'_1$ , and the left subtree begins with a line of  $\vee$  gate whose other input is the constant 0. The number of the  $\vee$  gates is equal to the depth of the tree  $Q'_1$ . Proceeding just as before, we finish up the construction of  $T'$  by extending the last node on the line of  $\vee$  gates that corresponds to the tree  $Q'_m$  with a line of  $v$   $\vee$  nodes whose other inputs are the literals  $q_1, \dots, q_v$ .

Clearly, the tree  $T'$  thus constructed is binary and

$$\begin{aligned} w(T') &\leq \max\{\max\{w(P'_i) \mid 1 \leq i \leq k\}, \max\{w(Q'_j) \mid 1 \leq j \leq m\}\} + 2 \\ &\leq 2(w-1) + 2 = 2w, \end{aligned}$$

$$\begin{aligned} s(T') &\leq \sum_{i=1}^k 2s(P'_i) + u + \sum_{j=1}^m 2s(Q'_j) + v \\ &\leq 2\left(\sum_{i=1}^k s(P'_i) + \sum_{j=1}^m s(Q'_j) + n\right) \\ &\leq 2\left(2^{w-1}n \sum_{i=1}^k s(P_i) + 2^j w - 1n \sum_{j=1}^m s(Q_j) + n\right) \\ &\leq 2^w n \left(\sum_{i=1}^k s(P_i) + \sum_{j=1}^m s(Q_j) + 1\right) \\ &\leq 2^w ns. \quad \square \end{aligned}$$

As an aside and no surprise, note that the above lemma shows that the power of polynomial-size binary and general formulas is the same provided their width is bounded or logarithmic.

**Lemma 2.** *A formula of depth  $d$  and size  $s$  on  $n$  variables can be computed by a binary formula of width  $2d + 1$  and size  $4^d(n + 1)(s + 1)$ .*

**Proof.** We argue by induction on the depth  $d$ . For  $d = 0$  the claim is obvious. For the induction step, suppose  $F$  is a formula of depth  $d > 1$  and size  $s$ . Let  $T$  be the tree that represents  $F$ . Denote by  $T_1, \dots, T_k$  the subtrees rooted at the children of the root of  $T$  whose depth is greater than 0. Also, denote  $t_1, \dots, t_l$  the depth-0 children of the root of  $T$ , i.e., all the literals feeding into it. We can assume that  $l \leq n$  and no constants appear as children of the root of  $T$ , since otherwise the formula is determined and we are done.

Since the depth of  $T_1, \dots, T_k$  is less than  $d$ , by the induction hypothesis we have equivalent binary formulas  $T'_1, \dots, T'_k$  such that  $w(T'_i) \leq 2d - 1$  and  $s(T'_i) \leq 4^{d-1}(n + 1)(s(T_i) + 1)$ , ( $i = 1, \dots, k$ ).

Now, to construct the desired tree  $T'$  equivalent to  $T$  we proceed in pretty much the same way as in the proof of Lemma 1. The root of  $T'$  is the same gate as the root of  $T$ , say an  $\vee$  gate. The right subtree of the root is  $T'_1$ , and the left subtree begins with a line of  $\vee$  gates whose other input is the constant 0. The number of the  $\vee$  gates on the line is equal to the depth of  $T'_1$ . Next, the last gate on the line has as its right subtree the tree  $T'_2$ , and the left subtree begins with a line of  $\vee$  gates whose other input is 0 and whose number is equal to the depth of  $T'_2$ . The process is clear now for each of the rest of  $T'_3, \dots, T'_k$ . To finish up the construction, we extend the last gate on the line associated with  $T'_k$  to a line of  $\vee$  gates whose other inputs are the literals  $t_1, \dots, t_l$ .

Clearly the tree  $T'$  thus constructed is binary and

$$w(T') \leq \max\{w(T'_i) \mid 1 \leq i \leq k\} + 2 \leq (2d - 1) + 2 = 2d + 1,$$

$$\begin{aligned} s(T') &\leq \sum_{i=1}^k 2s(T'_i) + l \leq \sum_{i=1}^k 2 \cdot 4^{d-1}(n + 1)(s(T_i) + 1) + n \\ &\leq 2 \cdot 4^{d-1}(n + 1) \left( \sum_{i=1}^k s(T_i) + k \right) + n \\ &\leq 2 \cdot 4^{d-1}(n + 1) \cdot 2s + n = 4^d(n + 1)s + n \\ &\leq 4^d(n + 1)(s + 1). \quad \square \end{aligned}$$

**Lemma 3.** *A binary formula of width  $w$  and size  $s$  can be computed by an unbounded fan-in circuit of depth  $2w$  and size  $s$ .*

**Proof.** Let  $F$  be a formula represented by a binary tree  $T$  that has the width  $w$  and size  $s$ . By induction on  $w$ , we will construct a circuit  $C$  of depth at most  $2w$ , the size at most  $s$ , and such that  $C$  computes  $F$ .

The base case  $w = 1$  is trivial. For  $w > 1$ , consider the trunk of the tree  $T$ . It contains a sequence of  $\vee$  and  $\wedge$  gates in any order. For the sake of concreteness, suppose that from the root of  $T$  down the trunk there first is a run of  $g_1$   $\vee$  gates, then a run of  $g_2$   $\wedge$  gates, and so on alternating until the end of the trunk. Further suppose that the trunk ends with, say, a run of  $g_r$   $\vee$  gates. Thus, the trunk is a sequence of  $r$  alternating runs of  $\vee$  or  $\wedge$  gates, it starts and ends with a run of  $\vee$  gates, and the  $k$ th run has  $g_k$  gates ( $k = 1, \dots, r$ ). With these assumptions we have that  $r$  is an odd number, i.e.,  $r = 2m + 1$  for some  $m = 0, 1, 2, \dots$

Let  $T_1^k, T_2^k, \dots, T_{g_k}^k$  be the subtrees that feed into the gates of the  $k$ th run ( $k = 1, \dots, r$ ). If  $T_1^k, T_2^k, \dots, T_{g_k}^k$  denote also formulas computed by the corresponding subtrees, we can write

$$F = T_1^1 \vee \dots \vee T_{g_1}^1 \vee (T_1^2 \wedge \dots \wedge T_{g_2}^2 \wedge (\dots \wedge (T_1^r \vee \dots \vee T_{g_r}^r) \dots)). \quad (1)$$

By applying the distributive law and expanding the expression on the right-hand side of (1) as much as possible, we have

$$F = T_1^1 \vee \dots \vee T_{g_1}^1 \vee \left( \bigvee_{i=1}^m \left( \bigwedge_{j=1}^{g_{2i+1}} T_1^{2i} \wedge \dots \wedge T_{g_2}^{2i} \wedge T_1^{4i} \wedge \dots \wedge T_{g_4}^{4i} \wedge \dots \wedge T_1^{2i} \wedge \dots \wedge T_{g_{2i}}^{2i} \wedge T_j^{2i+1} \right) \right). \quad (2)$$

Now we build the circuit  $C$  that computes the right-hand side of (2) as follows. The root is an  $\vee$  gate, and the next level consists of  $g_3 + g_5 + \dots + g_r$   $\wedge$  gates. Below these come the circuits for every tree  $T_1^k, T_2^k, \dots, T_{g_k}^k$ , ( $k = 1, \dots, r$ ), given by the induction hypothesis. If we index the  $\wedge$  gates on the first level by  $(i, j)$ , where  $i = 1, \dots, m$  and  $j = 1, \dots, g_{2i+1}$ , then the  $(i, j)$ th  $\wedge$  gate connects to the roots of the circuits for  $T_1^{2i}, \dots, T_{g_2}^{2i}, T_1^{4i}, \dots, T_{g_4}^{4i}, \dots, T_1^{2i}, \dots, T_{g_{2i}}^{2i}, T_j^{2i+1}$ , thus computing the  $(i, j)$ th product from (2). Finally, the top  $\vee$  gate is connected to the roots of the equivalent circuits for  $T_1^1, \dots, T_{g_1}^1$  and every  $\wedge$  gate on the first level.

Therefore, the circuit  $C$  correctly computes the formula  $F$  and by induction has the depth at most  $2(w - 1) + 2 = 2w$ . Moreover, the size of  $C$  is

$$s(C) \leq \sum_{i=1}^r \sum_{j=1}^{g_i} s(T_j^i) + g_3 + g_5 + \dots + g_r + 1 \leq s. \quad \square$$

#### 4. The result

The next theorem shows that the complexity classes  $AC^0$  and  $NC^1$  consist precisely of, respectively, constant and  $O(\log n)$  width polynomial-size Boolean formulas.

**Theorem 4.**  $AC^0 = BFW^0$  and  $NC^1 = BFW^1$ .

**Proof.** The inclusion  $BFW^0 \subseteq AC^0$  is a consequence of Lemma 1 and Lemma 3. To see the converse  $AC^0 \subseteq BFW^0$ , we first observe that an  $AC^0$  circuit of depth  $d$  and size  $s$  can easily be made into a formula of depth  $d$  and size  $O(s^d)$ . Now the inclusion follows from Lemma 2.

To prove  $NC^1 = BFW^1$  we use the fact that  $NC^1$  consists of polynomial-size  $O(\log n)$  depth formulas, which in turn have the same power as the polynomial-size formulas. Then  $BFW^1 \subseteq NC^1$  is trivial, and  $NC^1 \subseteq BFW^1$  follows from Lemma 2.  $\square$

#### Acknowledgment

We would like to thank David Barrington for many helpful discussions. His valuable ideas and comments were influential to the paper. Suggestions generated in discussions with Steve Lindell are also acknowledged with pleasure.

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